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Sep. 11 2002 02:12PM P2

PATENT APPLICATION

Attorney Docket: 54364

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Gupta, et al.

Serial No.:

09/847,667

Filed:

For:

5/1/01

Method for Bonding Wafers to

Produce Stacked Integrated Circuits

Group Art Unit:

2827

Examiner: James Mitchell

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TECHNOLOGY CENTER 2800

HON. ASSISTANT COMMISSIONER OF PATENTS AND TRADEMARKS Washington, DC 20231

TRANSMITTAL OF AMENDMENT AND PETITION FOR EXTENSION OF TIME

In response to the office action dated 3/13/02, Applicant submits herewith an amendment in the above-identified patent application and petitions the Commissioner for a three month extension of time. Applicant is a Small Entity. Hence, Applicant belives the required fee is \$460. The Commissioner is hereby authorized to charge this fee and any additional fees which may be required in this application under 37 C.F.R. Section 1.16-1.17 during its entire pendency, or credit any overpayment, to Deposit Account No. 23-0424. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 23-0424. This sheet is filed in duplicate.

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Calvin B. Ward

Registration No. 30,896

Date: September 11, 2002:

Tols. U.L

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